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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re application of: Rinerson et al.

Attorney Docket No.: UNTYP020

Application No.: 10/634,636

Examiner: Not Yet Assigned

Filed: August 4, 2003

Group: 2818

Title: A 2-TERMINAL TRAPPED CHARGE MEMORY DEVICE WITH VOLTAGE

SWITCHABLE MULTI-LEVEL RESISTANCE

Confirmation No.: 7199

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being transmitted by feesimile to (703) 746-4060 to the U.S. Patent and Trademark Office on November 18, 2003.

Signed:

Laura McDean

REQUEST FOR CORRECTED FILING RECEIPT

Mail Stop Missing Parts Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The Applicants in the above-identified application respectfully request for the issuance of a corrected filing receipt. The correct filing date of the above-identified application is August 4, 2003.

Attached please find (1) a copy of the official filing receipt received from the Patent and Trademark Office in the above application showing the disputed date of filing (August 5, 2003), (2) a copy of the Express Mail receipt showing the date of mailing (August 4, 2003), (3) a copy of the signed Express Mail certificate showing the date of mailing (August 4, 2003) and (4) a print out from www.usps.com showing that Express Mail Receipt No.: EV333981848US was accepted for mailing on August 4, 2003. The return postcard and the filing receipt from the Patent and Trademark Office do not reflect the proper date of filing under 37 CFR § 1.10. The Applicants therefore respectfully request that the application be accorded the correct date of filing, as shown on the Express Mail receipt and the Express Mail certificate, of August 4, 2003.

This correction is not due to any error by the Applicants. The Applicants therefore

respectfully request that any and allifees related to this request be waived. However, if it is

determined that any fee are due in connection with the filing of this request; the Commissioner

hereby authorized to charge such fees to Deposit Account 500388 (Order No. UNTYP020).

Respectfully submitted,

BEYER WEAVER & THOMAS, LLP

Morgan B. Malino

Registration No. 41,177

P.O. Box 778
Berkeley, CA 94704-0778

BEYER WEAVER & THOMAS LLP

BERKELEY, CA 94704-0778



Page 1 of 2



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P.O. BOX 778

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	APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
	10/634,636	08/05/2003	2818	0.00	UNTYP020	10	56	2

CONFIRMATION NO. 7199

FILING RECEIPT OC000000011167217

Date Mailed: 11/03/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by

check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Darrell Rinerson, Cupertino, CA; Wayne Kinney, Emmett, ID: Steven W. Longcor, Mountain View, CA; Edmond R. Ward, Monte Sereno, CA; Steve Kuo-Ren Hsia, San Jose, CA; Christophe J. Chevallier, Palo Alto, CA:

Assignment For Published Patent Application

Unity Semiconductor Corporation, Sunnyvale, CA:

Domestic Priority data as claimed by applicant

This application is a CIP of 10/249,846 05/12/2003 which claims benefit of 60/400,849 08/02/2002 and claims benefit of 60/422,922 10/31/2002

Foreign Applications

If Required, Foreign Filing License Granted: 11/03/2003

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No



** SMALL ENTITY ** ______ THE PROPERTY OF THE

2-Terminal trapped charge memory device with voltage switchable multi-level resistance

Preliminary Class

365

LICENSE FOR FOREIGN FILING UNDER Title 35, United States Code, Section 184 Title 37, Code of Federal Regulations, 5.11 & 5.15

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PAGE 12/14 * RCVD AT 11/18/2003 4:46:11 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-2/3 * DNIS:7464060 * CSID:16509618301 * DURATION (mm-ss):03-34

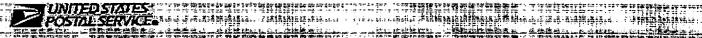
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
THE THE PARTY OF EXPRESS MAILING AND THE PARTY DOCKE NO. UNITYP020
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UTILITY PATENT APPLICATION TRANSMITTAL (37 CFR. § 1.53(b)) (Continuation, Divisional or Continuation-in-part application)
Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450
Sir. This is a request for filing a patent application under 37 CFR. § 1.53(b) in the name of inventors: Darrell Rinerson, Wayne Kinney, Steven W. Longcor, Edmond R. Ward, Steve Kuo-Ren Hsia and Christophe J. Chevallier
For: A 2-TERMINAL TRAPPED CHARGE MEMORY DEVICE WITH VOLTAGE SWITCHABLE MULTI-LEVEL RESISTANCE
Assigned to: Unity Semiconductor Corporation This application is a Continuation Divisional Continuation-in-part
of prior Application No.: 10/249,846, from which priority under 35 U.S.C. §120 is claimed. Application No.
10/249,846 is a non-provisional of 60/400,849, filed on August 2, 2002. Additionally, Application No.
10/249,846 is a non-provisional of 60/422,922, filed on October 31, 2002.
Application Elements:
30 Pages of Specification, Claims and Abstract
10 Sheets of formal Drawings
Declaration
Newly executed Copy from a prior application (37 CFR 1.63(d) for a continuation or divisional).
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(Revised 04/03, Pat App Trans 53(b) ContDivCIP)

Page I

The entire disclosure of the prior application from which a copy of the declaration is herein supplied is considered as being part of the disclosure of the accompanying

application and is hereby incorporated by reference therein.



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